

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (currently amended) A voltage regulator circuit arrangement comprising a voltage regulator for generating an output voltage in dependence of a reference signal, characterized in that a reference signal generation circuit is provided for generating said reference signal comprising a plurality of inputs connected to internal terminals, whereby a sub-set of said plurality internal terminals is connected to an external terminal- and said reference signal generation circuit comprises a selection circuit for selecting said reference signal out of a range of possible reference signals in dependence upon a selection signal received at said external terminal.
2. (canceled)
3. (currently amended) A voltage regulator circuit arrangement as claimed in claim-~~2~~ 1, characterized in that said reference signal generation circuit comprises a comparator with an input connected to an internal terminal out of said sub-set of internal terminals for comparing said selection signal with a threshold signal and an output connected to said selection circuit.
4. (original) A voltage regulator circuit arrangement as claimed in claim 3, characterized in that said reference signal generation circuit comprises a further comparator with an input connected to a further internal terminal out of said sub-set of internal terminals for comparing said selection signal with a further threshold signal and an output connected to said selection circuit.

5. (currently amended) A voltage regulator circuit arrangement as claimed in claim-2 1, characterized in that said plurality of internal terminals comprises a further sub-set of internal terminals connected to a further external terminal for receiving a further selection signal, whereby said reference signal generation circuit comprises a further comparator with an input connected to an internal terminal out of said further sub-set of internal terminals for comparing said further selection signal with a further threshold signal and an output connected to said selection circuit.

6. (original) A voltage regulator circuit arrangement as claimed in claim 1, characterized in that said reference signal generation circuit comprises a voltage divider circuit whereby said inputs correspond to the inputs said voltage divider circuit and said reference signal is provided at an output of said voltage divider circuit.

7. (original) A voltage regulator circuit arrangement as claimed in claim 6, characterized in that said voltage divider circuit is a resistive ladder network.

8. (original) A voltage regulator circuit arrangement as claimed in claim 7, characterized in that a said selection of internal terminals connected to said external terminal short circuits a section of said resistive ladder network.

9. (previously presented) A voltage regulator circuit arrangement as claimed in claim 1, characterized in that said voltage regulator comprises an output for providing said output voltage, whereby said output is connected to an internal terminal out of said plurality of internal terminals.

10. (currently amended) An integrated circuit comprising a voltage regulator circuit comprising a voltage regulator for generating an output voltage in dependence of a reference signal, characterized in that a reference signal generation circuit is provided for generating said reference signal comprising a plurality of inputs connected to terminals of said integrated circuit- and a selection circuit for selecting said reference signal out of a

range of possible reference signals in dependence upon a selection signal received at an external terminal of said integrated circuit.

11. (new) The integrated circuit of claim 10, wherein said reference signal generation circuit comprises a comparator with an input connected to an internal terminal out of a sub-set of internal terminals of the integrated circuit for comparing said selection signal with a threshold signal and an output connected to said selection circuit.